



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hidetoshi Nishikawa

Art Unit : Unknown

Serial No. : 10/561,552

Examiner : Unknown

Filed : December 20, 2005

Title : SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

MAIL STOP

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

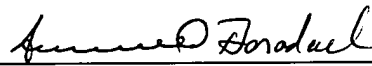
TRANSMITTAL OF SUBSTITUTE DECLARATION AND POWER OF ATTORNEY

Please substitute the enclosed Declaration and Power of Attorney (3 pages) for the corresponding Declaration and Power of Attorney previously filed in the application.

Please apply any charges or credits to Deposit Account No. 06 1050.

Respectfully submitted,

Date: 3/15/06


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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

3/15/06
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